X-1337 US

PATENT

IN THE UNITED STATES PATENT OFFICE

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| Applicant: | Gareth D. Edwards |
| Assignee: | Xilinx, Inc. |
| Title: | "Scheme for Eliminating the Effects of Duty Cycle Asymmetry in Clock-Forwarded Double Data Rate Interface Applications" |

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| Serial No.: | 10/703,813 | File Date: | 11-12-2003 |
| Examiner: | Fritz Alphonse | Art Unit: | 2133 |
| Docket No.: | X-1337 US | Conf. No.: | 6961 |

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## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT FILED WITH RCE

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the one (1) reference listed in the attached Substitute for Form 1449A/PTO. A copy is enclosed herein.

This Information Disclosure Statement is being filed with an RCE under 37 CFR 1.114 along with the fee requirements set forth under 37 CFR 1.17(e).

Citation of the above document shall not be construed as an admission that the document is necessarily prior art with respect to the instant invention. Citation of the above document shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above document shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Justin Liu
Attorney for Applicant
Reg. No. 51,959

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on March 6, 2007.

Julie Matthews
Name

Signature

JL: jam

PTO/SB/08A (10-01)
Approved for use through 10/31/2002. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE


Substitute for form 1449A/PTO

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

| Complete if Known | |
|---|---|
| Application / Conf. No. | 10/706,813 / 6961 |
| Filing Date | November 12, 2003 |
| First Named Inventor | Gareth D. Edwards |
| Art Unit | 2133 |
| Examiner Name | Fritz Alphonse |
| Attorney Docket Number | X-1337 US |

## OTHER -- NON PATENT LITERATURE DOCUMENTS

| Examiner Initials * | Cite No[1] | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T[2] |
|---|---|---|---|
| | | RHODES, MARTIN; "XGMII Using the DDR Registers, DCM, and SelectI/O-Ultra Features", XAPP606, July 10, 2002, pp. 1-11, v1.1, available from Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124 | |
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| Examiner Signature | | Date Considered | |
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1] Applicant's unique citation designation number. [2] Applicant is to place a check mark here if English language Translation is attached.